

**REMARKS**

In the foregoing, a typographical error in paragraph [00119] of the specification has been corrected, supported by the disclosure in FIG. 18 at timing "b", at which pulse PS is applied to the display electrode X. Accordingly, it is submitted to be clear that no new matter is presented.

Further, FIG. 10 is changed as shown in red ink to correct for two related typographical errors and a corrected formal sheet of drawings including FIGS. 9 and 10 is submitted. Additionally, corrections are made in the drawings of FIGS. 19A, 19C, and 19F as shown by red ink annotations, as explained above.

In accordance with the foregoing, amendments to improve form are made to various of the pending claims 1-8 of this application and new claims 9 to 11 are presented. No new matter is presented.

Approval and entry of the foregoing claims are respectfully requested.

If there are any additional fees associated with filing of this Preliminary Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

STAAS & HALSEY LLP

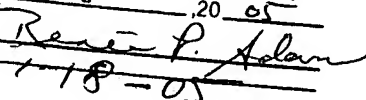
Date: January 18, 2005

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**CERTIFICATE UNDER 37 CFR 1.8(a)**  
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 1-18, 2005  
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By:   
Date: 1-18-05

**AMENDMENTS TO THE DRAWINGS:**

The attached drawing(s) include changes to FIGS. 19A, 19C and 19F. The sheet containing FIGS. 19A, 19C, and 19F replaces the original sheet including FIGS. 19A-19H.

As explained in the specification paragraph [00116], a positive discharge sustain Voltage  $V_s$  is applied to both display electrodes X and Y, resulting in a positive pulse base potential  $+V_s$  on both the X and Y display electrodes. As next explained in paragraph [00117], a pulse PW applied to the display electrode X places it at a predetermined negative potential  $-V_w$ , as shown in FIG. 19A. However, no change has occurred in the potential on the Y electrode which accordingly remains at a predetermined positive voltage  $+V_s$ . Accordingly, in FIG. 19A, on the electrode Y the incorrect  $-V_s$  has been changed to  $+V_s$ . The respective, opposite polarities of the voltages on electrodes X and Y shown in FIG. 19A, furthermore, are consistent with the respective, opposite polarity charges stacked on the surface of the dielectric layer above the display electrodes X and Y - - i.e., positive charges are stored above the X electrode and negative charges are stored above the Y electrode, as explained in paragraph [00118].

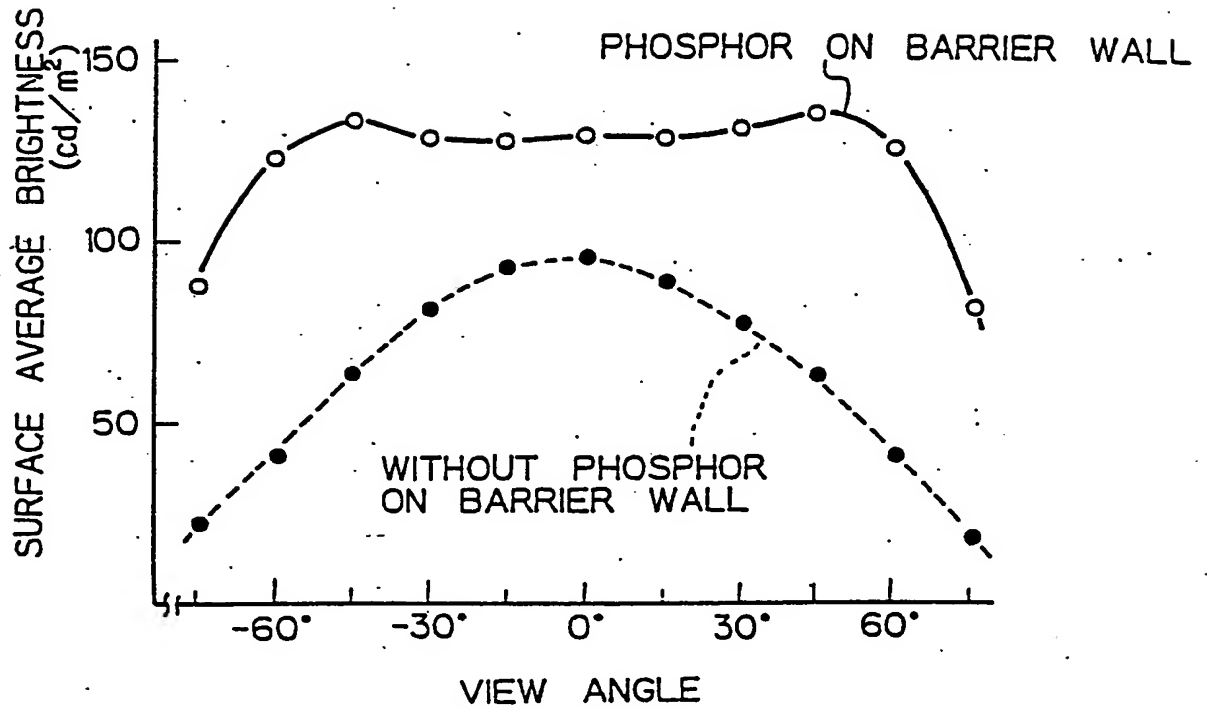
The change in FIG. 19C, similarly, is consistent with the explanation of operation in paragraph [00120], in which the respective (" $+$ " and " $0$ ") charges on electrodes X and Y shown in FIG. 19B are reversed (i.e., to " $0$ " and " $+$ ") respectively on the X and Y electrodes in FIG. 19C, as now corrected.

Finally, the respective polarities of the charges on electrodes X and Y in FIG. 19F are reversed from those in FIG. 19E and accordingly, a " $0$ " should appear beneath the electrode X in FIG. 19F, as shown in the corrected FIG. 19F; see, paragraphs [00124] and [00125].

Accordingly, no new matter is presented in the above corrections to FIGS. 19A, 19C, and 19F and approval and entry of the Replacement Sheet of FIGS. 19A to 19H are respectfully requested.

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Fig.9



R/ Fig.10

